

Attorney Docket No.: 2102397-991180

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: William John Saiki et al.

Serial No.: 10/044,273

Group Art Unit: 2643

Filed: January 10, 2002

Examiner: Not Yet Assigned

Title: **HIGH VOLTAGE GENERATION AND REGULATION SYSTEM FOR DIGITAL MULTILEVEL NONVOLATILE MEMORY**

Certificate of Mailing

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as FIRST CLASS MAIL in an envelope addressed to: BOX MISSING PARTS, Commissioner of Patents, Washington, D.C. 20231, on:

April 3, 2002

*Rosa A. Caviedes*

Rosa A. Caviedes

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RESPONSE TO NOTICE TO FILE CORRECTED APPLICATION PAPERS

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

In response to the "Notice to File Corrected Application Papers" mailed by the United States Patent and Trademark Office on February 25, 2002, the following documents are enclosed to complete the filing of the above-referenced patent application:

1. Seventeen (17) sheets of corrected informal drawings;
2. Copy of Notice to File Corrected Application Papers (1 pg.); and
2. Return Postcard.

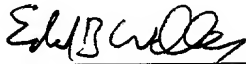
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The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment to Deposit Account No. 07-1896.

Respectfully submitted,

GRAY CARY WARE & FREIDENRICH LLP

Dated: April 3, 2002

By   
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